

# 74HC4002

## Dual 4-input NOR gate

Rev. 7 — 13 September 2021

Product data sheet

## 1. General description

The 74HC4002 is a dual 4-input NOR gate. Inputs also include clamp diodes that enable the use of current limiting resistors to interface inputs to voltages in excess of  $V_{CC}$ .

## 2. Features and benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- CMOS low power dissipation
- High noise immunity
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- CMOS input levels
- Complies with JEDEC standards:
  - JESD8C (2.7 V to 3.6 V)
  - JESD7A (2.0 V to 6.0 V)
- ESD protection:
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +80 °C and from -40 °C to +125 °C.

## 3. Ordering information

Table 1. Ordering information

| Type number | Package           |         |   |          |
|-------------|-------------------|---------|---|----------|
|             | Temperature range | Name    | Description   | Version  |
| 74HC4002D   | -40 °C to +125 °C | SO14    | plastic small outline package; 14 leads;<br>body width 3.9 mm             | SOT108-1 |
| 74HC4002PW  | -40 °C to +125 °C | TSSOP14 | plastic thin shrink small outline package; 14 leads;<br>body width 4.4 mm | SOT402-1 |

## 4. Functional diagram

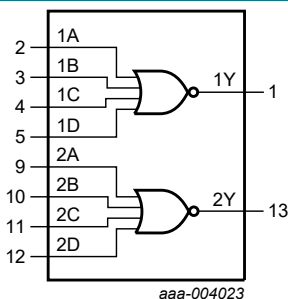


Fig. 1. Functional diagram

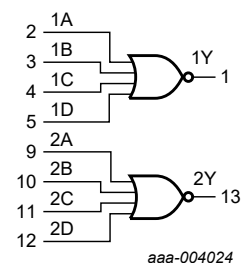


Fig. 2. Logic symbol

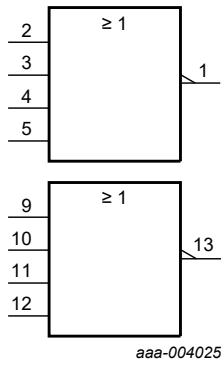


Fig. 3. IEC Logic symbol

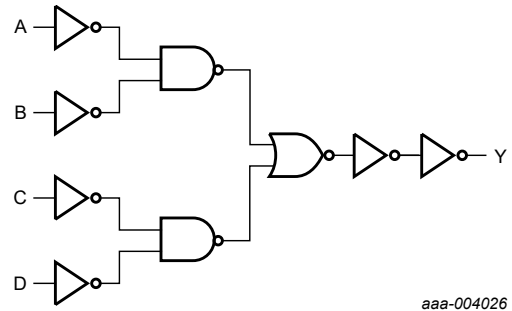


Fig. 4. Logic diagram

## 5. Pinning information

### 5.1. Pinning

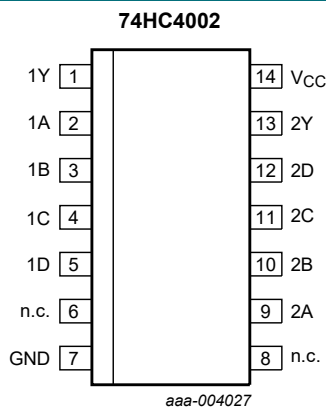


Fig. 5. Pin configuration SOT108-1 (SO14)

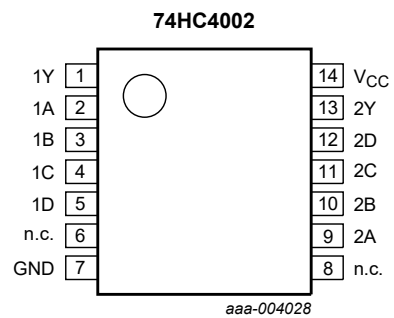


Fig. 6. Pin configuration SOT402-1 (TSSOP14)

### 5.2. Pin description

Table 2. Pin description

| Symbol          | Pin           | Description    |
|-----------------|---------------|----------------|
| 1Y, 2Y          | 1, 13         | data output    |
| 1A, 1B, 1C, 1D  | 2, 3, 4, 5    | data input     |
| n.c.            | 6, 8          | not connected  |
| GND             | 7             | ground (0 V)   |
| 2A, 2B, 2C, 2D  | 9, 10, 11, 12 | data input     |
| V <sub>CC</sub> | 14            | supply voltage |

## 6. Functional description

**Table 3. Function table**

*H = HIGH voltage level; L = LOW voltage level; X = don't care.*

| Input |    |    |    | Output |
|-------|----|----|----|--------|
| nA    | nB | nC | nD | nY     |
| L     | L  | L  | L  | H      |
| H     | X  | X  | X  | L      |
| X     | H  | X  | X  | L      |
| X     | X  | H  | X  | L      |
| X     | X  | X  | H  | L      |

## 7. Limiting values

**Table 4. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).*

| Symbol    | Parameter               | Conditions   | Min  | Max      | Unit |
|-----------|-------------------------|--|------|----------|------|
| $V_{CC}$  | supply voltage          |  | -0.5 | +7       | V    |
| $I_{IK}$  | input clamping current  | $V_I < -0.5\text{ V}$ or $V_I > V_{CC} + 0.5\text{ V}$ [1] | -    | $\pm 20$ | mA   |
| $I_{OK}$  | output clamping current | $V_O < -0.5\text{ V}$ or $V_O > V_{CC} + 0.5\text{ V}$ [1] | -    | $\pm 20$ | mA   |
| $I_O$     | output current          | $-0.5\text{ V} < V_O < V_{CC} + 0.5\text{ V}$              | -    | $\pm 25$ | mA   |
| $I_{CC}$  | supply current          |  | -    | 50       | mA   |
| $I_{GND}$ | ground current          |  | -50  | -        | mA   |
| $T_{stg}$ | storage temperature     |  | -65  | +150     | °C   |
| $P_{tot}$ | total power dissipation | [2]  | -    | 500      | mW   |

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT108-1 (SO14) package:  $P_{tot}$  derates linearly with 10.1 mW/K above 100 °C.  
For SOT402-1 (TSSOP14) package:  $P_{tot}$  derates linearly with 7.3 mW/K above 81 °C.

## 8. Recommended operating conditions

**Table 5. Recommended operating conditions**

*Voltages are referenced to GND (ground = 0 V)*

| Symbol              | Parameter                           | Conditions              | Min | Typ  | Max      | Unit |
|---------------------|-------------------------------------|-------------------------|-----|------|----------|------|
| $V_{CC}$            | supply voltage                      |                         | 2.0 | 5.0  | 6.0      | V    |
| $V_I$               | input voltage                       |                         | 0   | -    | $V_{CC}$ | V    |
| $V_O$               | output voltage                      |                         | 0   | -    | $V_{CC}$ | V    |
| $T_{amb}$           | ambient temperature                 |                         | -40 | +25  | +125     | °C   |
| $\Delta t/\Delta V$ | input transition rise and fall rate | $V_{CC} = 2.0\text{ V}$ | -   | -    | 625      | ns/V |
|                     |                                     | $V_{CC} = 4.5\text{ V}$ | -   | 1.67 | 139      | ns/V |
|                     |                                     | $V_{CC} = 6.0\text{ V}$ | -   | -    | 83       | ns/V |

## 9. Static characteristics

**Table 6. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol          | Parameter                 | Conditions   | 25 °C |      |      | -40 °C to +85 °C |      | -40 °C to +125 °C |      | Unit |
|-----------------|---------------------------|--|-------|------|------|------------------|------|-------------------|------|------|
|                 |                           |  | Min   | Typ  | Max  | Min              | Max  | Min               | Max  |      |
| V <sub>IH</sub> | HIGH-level input voltage  | V <sub>CC</sub> = 2.0 V  | 1.5   | 1.2  | -    | 1.5              | -    | 1.5               | -    | V    |
|                 |                           | V <sub>CC</sub> = 4.5 V  | 3.15  | 2.4  | -    | 3.15             | -    | 3.15              | -    | V    |
|                 |                           | V <sub>CC</sub> = 6.0 V  | 4.2   | 3.2  | -    | 4.2              | -    | 4.2               | -    | V    |
| V <sub>IL</sub> | LOW-level input voltage   | V <sub>CC</sub> = 2.0 V  | -     | 0.8  | 0.5  | -                | 0.5  | -                 | 0.5  | V    |
|                 |                           | V <sub>CC</sub> = 4.5 V  | -     | 2.1  | 1.35 | -                | 1.35 | -                 | 1.35 | V    |
|                 |                           | V <sub>CC</sub> = 6.0 V  | -     | 2.8  | 1.8  | -                | 1.8  | -                 | 1.8  | V    |
| V <sub>OH</sub> | HIGH-level output voltage | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                    |       |      |      |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 2.0 V                                       | 1.9   | 2.0  | -    | 1.9              | -    | 1.9               | -    | V    |
|                 |                           | I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 4.5 V                                       | 4.4   | 4.5  | -    | 4.4              | -    | 4.4               | -    | V    |
|                 |                           | I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 6.0 V                                       | 5.9   | 6.0  | -    | 5.9              | -    | 5.9               | -    | V    |
|                 |                           | I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 4.5 V                                      | 3.98  | 4.32 | -    | 3.84             | -    | 3.7               | -    | V    |
|                 |                           | I <sub>O</sub> = -5.2 mA; V <sub>CC</sub> = 6.0 V                                      | 5.48  | 5.81 | -    | 5.34             | -    | 5.2               | -    | V    |
| V <sub>OL</sub> | LOW-level output voltage  | V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>                                    |       |      |      |                  |      |                   |      |      |
|                 |                           | I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 2.0 V  | -     | 0    | 0.1  | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 4.5 V  | -     | 0    | 0.1  | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 6.0 V  | -     | 0    | 0.1  | -                | 0.1  | -                 | 0.1  | V    |
|                 |                           | I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V                                       | -     | 0.15 | 0.26 | -                | 0.33 | -                 | 0.4  | V    |
|                 |                           | I <sub>O</sub> = 5.2 mA; V <sub>CC</sub> = 6.0 V                                       | -     | 0.16 | 0.26 | -                | 0.33 | -                 | 0.4  | V    |
| I <sub>I</sub>  | input leakage current     | V <sub>I</sub> = V <sub>CC</sub> or GND; V <sub>CC</sub> = 6.0 V                       | -     | -    | ±0.1 | -                | ±1   | -                 | ±1   | μA   |
| I <sub>CC</sub> | supply current            | V <sub>I</sub> = V <sub>CC</sub> or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 6.0 V | -     | -    | 2    | -                | 20   | -                 | 40   | μA   |
| C <sub>I</sub>  | input capacitance         |  | -     | 3.5  | -    | -                | -    | -                 | -    | pF   |

## 10. Dynamic characteristics

**Table 7. Dynamic characteristics**

GND = 0 V; C<sub>L</sub> = 50 pF; for test circuit see Fig. 8.

| Symbol          | Parameter         | Conditions                                      | 25 °C |     |     | -40 °C to +85 °C |     | -40 °C to +125 °C |     | Unit |
|-----------------|-------------------|---|-------|-----|-----|------------------|-----|-------------------|-----|------|
|                 |                   |   | Min   | Typ | Max | Min              | Max | Min               | Max |      |
| t <sub>pd</sub> | propagation delay | nA, nB, nC or nD to nY; see Fig. 7 [1]          |       |     |     |                  |     |                   |     |      |
|                 |                   | V <sub>CC</sub> = 2.0 V                         | -     | 30  | 100 | -                | 125 | -                 | 150 | ns   |
|                 |                   | V <sub>CC</sub> = 4.5 V                         | -     | 11  | 20  | -                | 25  | -                 | 30  | ns   |
|                 |                   | V <sub>CC</sub> = 6.0 V                         | -     | 9   | 17  | -                | 21  | -                 | 26  | ns   |
|                 |                   | V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF | -     | 9   | -   | -                | -   | -                 | -   | ns   |

| Symbol          | Parameter                     | Conditions   | 25 °C |     |     | -40 °C to +85 °C |     | -40 °C to +125 °C |     | Unit |
|-----------------|-------------------------------|--|-------|-----|-----|------------------|-----|-------------------|-----|------|
|                 |                               |  | Min   | Typ | Max | Min              | Max | Min               | Max |      |
| t <sub>t</sub>  | transition time               | nY; see Fig. 7 [2]                                       |       |     |     |                  |     |                   |     |      |
|                 |                               | V <sub>CC</sub> = 2.0 V                                  | -     | 19  | 75  | -                | 95  | -                 | 110 | ns   |
|                 |                               | V <sub>CC</sub> = 4.5 V                                  | -     | 7   | 15  | -                | 19  | -                 | 22  | ns   |
|                 |                               | V <sub>CC</sub> = 6.0 V                                  | -     | 6   | 13  | -                | 16  | -                 | 19  | ns   |
| C <sub>PD</sub> | power dissipation capacitance | per package; V <sub>I</sub> = GND to V <sub>CC</sub> [3] | -     | 16  | -   | -                | -   | -                 | -   | pF   |

- [1] t<sub>pd</sub> is the same as t<sub>PHL</sub> and t<sub>PLH</sub>.
- [2] t<sub>t</sub> is the same as t<sub>THL</sub> and t<sub>TLH</sub>.
- [3] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW):  
 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o)$  where:  
 f<sub>i</sub> = input frequency in MHz;  
 f<sub>o</sub> = output frequency in MHz;  
 C<sub>L</sub> = output load capacitance in pF;  
 V<sub>CC</sub> = supply voltage in V;  
 N = number of inputs switching;  
 $\sum(C_L \times V_{CC}^2 \times f_o)$  = sum of outputs.

### 10.1. Waveforms and test circuit

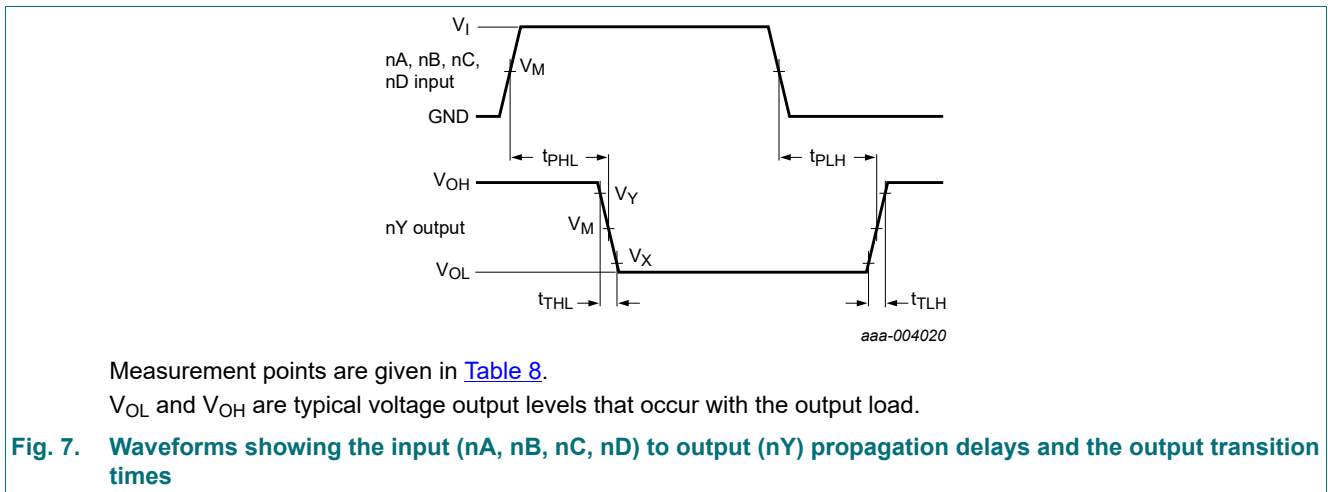
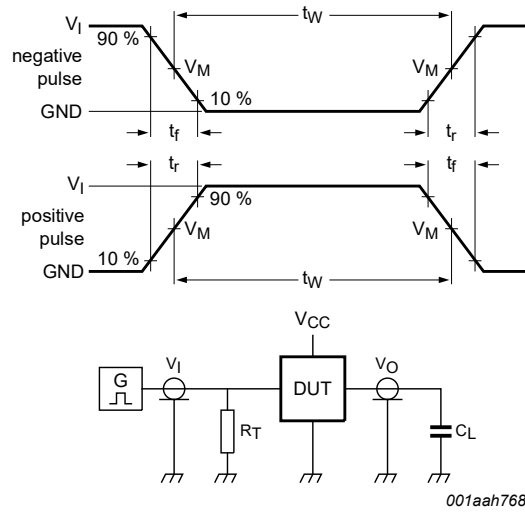


Table 8. Measurement points

| Input              | Output             |                    |                    |
|--------------------|--------------------|--------------------|--------------------|
| V <sub>M</sub>     | V <sub>M</sub>     | V <sub>X</sub>     | V <sub>Y</sub>     |
| 0.5V <sub>CC</sub> | 0.5V <sub>CC</sub> | 0.1V <sub>CC</sub> | 0.9V <sub>CC</sub> |



001aah768

Test data is given in [Table 9](#).

Definitions test circuit:

$R_T$  = termination resistance should be equal to output impedance  $Z_o$  of the pulse generator.

$C_L$  = load capacitance including jig and probe capacitance.

**Fig. 8. Test circuit for measuring switching times**

**Table 9. Test data**

| Input    |            | Load         | Test               |
|----------|------------|--------------|--------------------|
| $V_I$    | $t_r, t_f$ | $C_L$        |                    |
| $V_{CC}$ | 6.0 ns     | 15 pF, 50 pF | $t_{PLH}, t_{PHL}$ |

### 11. Package outline

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1

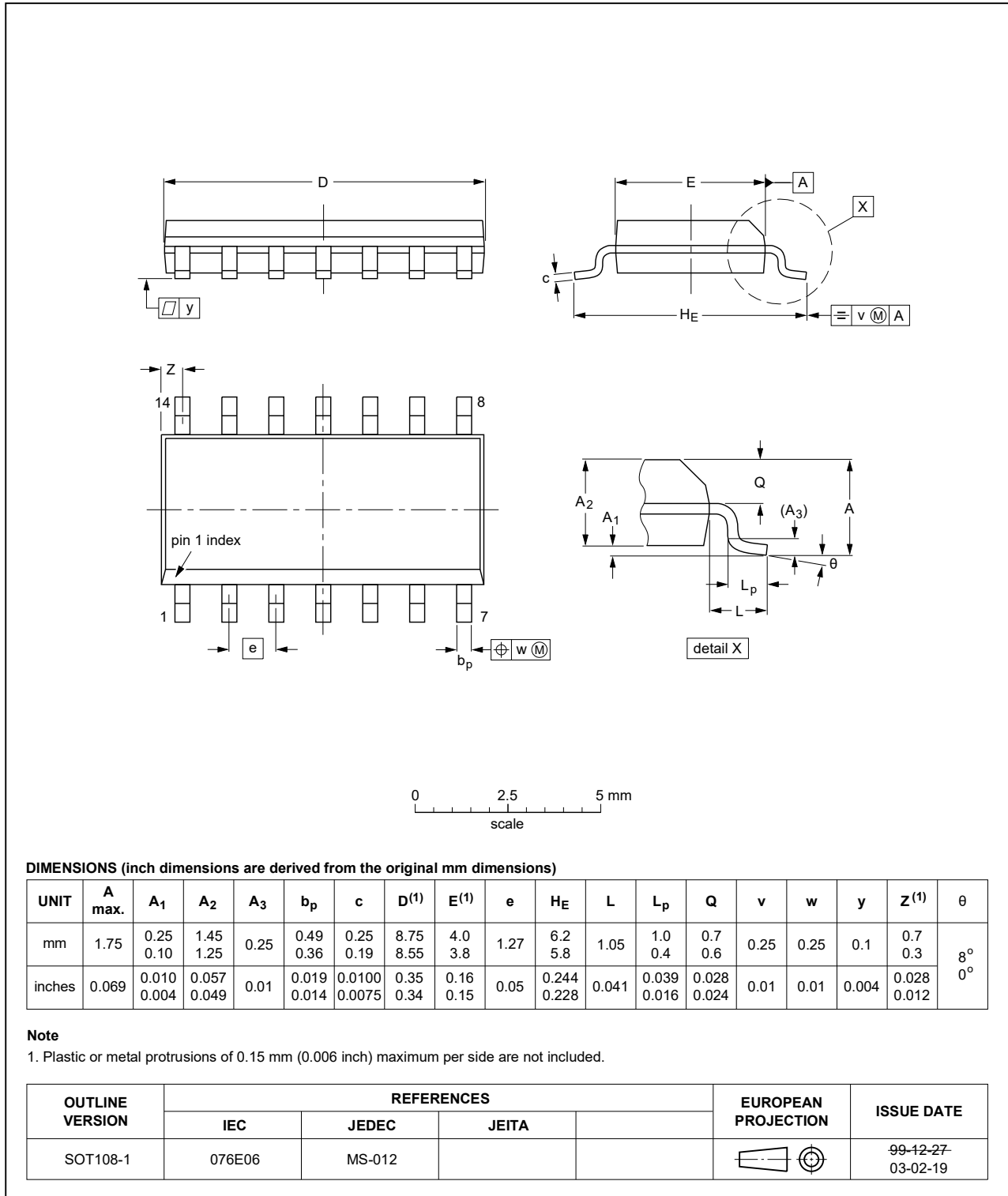


Fig. 9. Package outline SOT108-1 (SO14)

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1

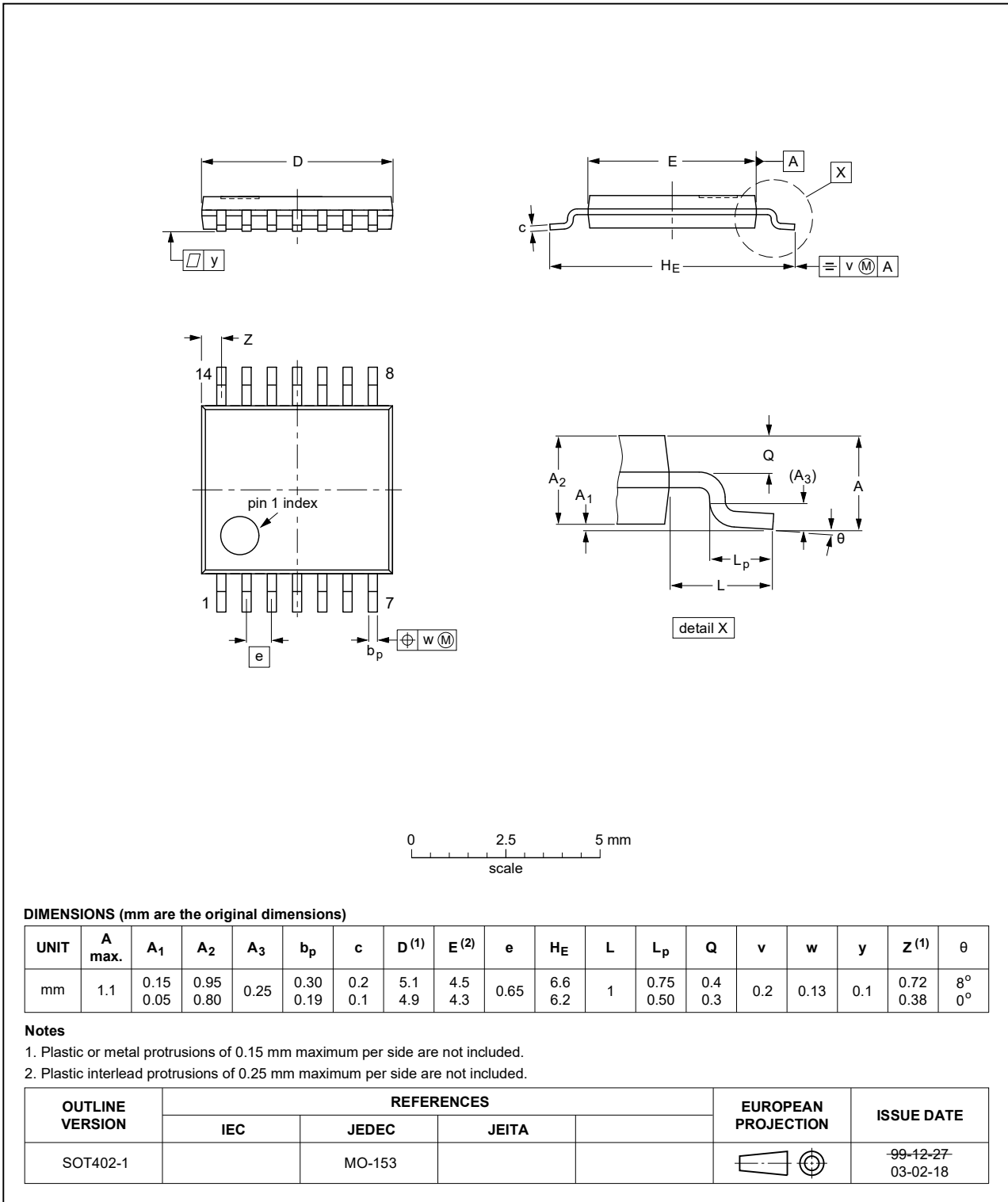


Fig. 10. Package outline SOT402-1 (TSSOP14)

## 12. Abbreviations

Table 10. Abbreviations

| Acronym | Description                             |
|---------|---|
| CMOS    | Complementary Metal Oxide Semiconductor |
| DUT     | Device Under Test                       |
| ESD     | ElectroStatic Discharge                 |
| HBM     | Human Body Model                        |
| MM      | Machine Model                           |

## 13. Revision history

Table 11. Revision history

| Document ID          | Release date   | Data sheet status     | Change notice | Supersedes           |
|----------------------|--|-----------------------|---------------|----------------------|
| 74HC4002 v.7         | 20210913   | Product data sheet    | -             | 74HC_HCT4002 v.6     |
| Modifications:       | <ul style="list-style-type: none"> <li>Type number 74HC4002DB (SOT337-1 / SSOP14) removed.</li> </ul>  |                       |               |                      |
| 74HC4002 v.6         | 20210311   | Product data sheet    | -             | 74HC_HCT4002 v.5     |
| Modifications:       | <ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li><a href="#">Section 2</a> updated.</li> <li><a href="#">Section 7</a>: Derating values for <math>P_{tot}</math> total power dissipation have changed.</li> <li>Type numbers 74HCT4002D (SOT108-1 / SO14) and 74HCT4002DB (SOT337-1 / SSOP14) removed.</li> </ul> |                       |               |                      |
| 74HC_HCT4002 v.5     | 20160526   | Product data sheet    | -             | 74HC_HCT4002 v.4     |
| Modifications:       | <ul style="list-style-type: none"> <li>Type numbers 74HC4002N and 74HCT4002N (SOT27-1) removed.</li> </ul>   |                       |               |                      |
| 74HC_HCT4002 v.4     | 20120917   | Product data sheet    | -             | 74HC_HCT4002 v.3     |
| Modifications:       | <ul style="list-style-type: none"> <li><a href="#">Table 1</a>: Type number 74HC20DB changed into 74HC4002DB.</li> </ul>   |                       |               |                      |
| 74HC_HCT4002 v.3     | 20120904   | Product data sheet    | -             | 74HC_HCT4002_CNV v.2 |
| Modifications:       | <ul style="list-style-type: none"> <li>The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>  |                       |               |                      |
| 74HC_HCT4002_CNV v.2 | 19970829   | Product specification | -             | -                    |

## 14. Legal information

### Data sheet status

| Document status [1][2]         | Product status [3] | Definition  |
|--------------------------------|--------------------|---|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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